

# Open Source LLVM-VPO Compiler

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## Project Overview

Media applications are becoming more complex; mobile devices are being built with low-power multi-issue mobile processors to manage increasing data and instruction flow. We're developing an Open Source Optimizing compiler from the LLVM [2] and Zephyr [3] Compiler frameworks so researchers can more easily implement code transformations for low-power Processors.

## Motivation

VPO w/ LCC	LLVM w/ Clang
<ul style="list-style-type: none"> <li>Preferred for machine level Optimizations</li> <li>No Support for new language standards</li> <li>Difficult to implement code-expander for new architectures</li> </ul>	<ul style="list-style-type: none"> <li>Preferred for high level Optimizations</li> <li>Support for new language standards</li> <li>Difficult to add support for new architectures</li> </ul>

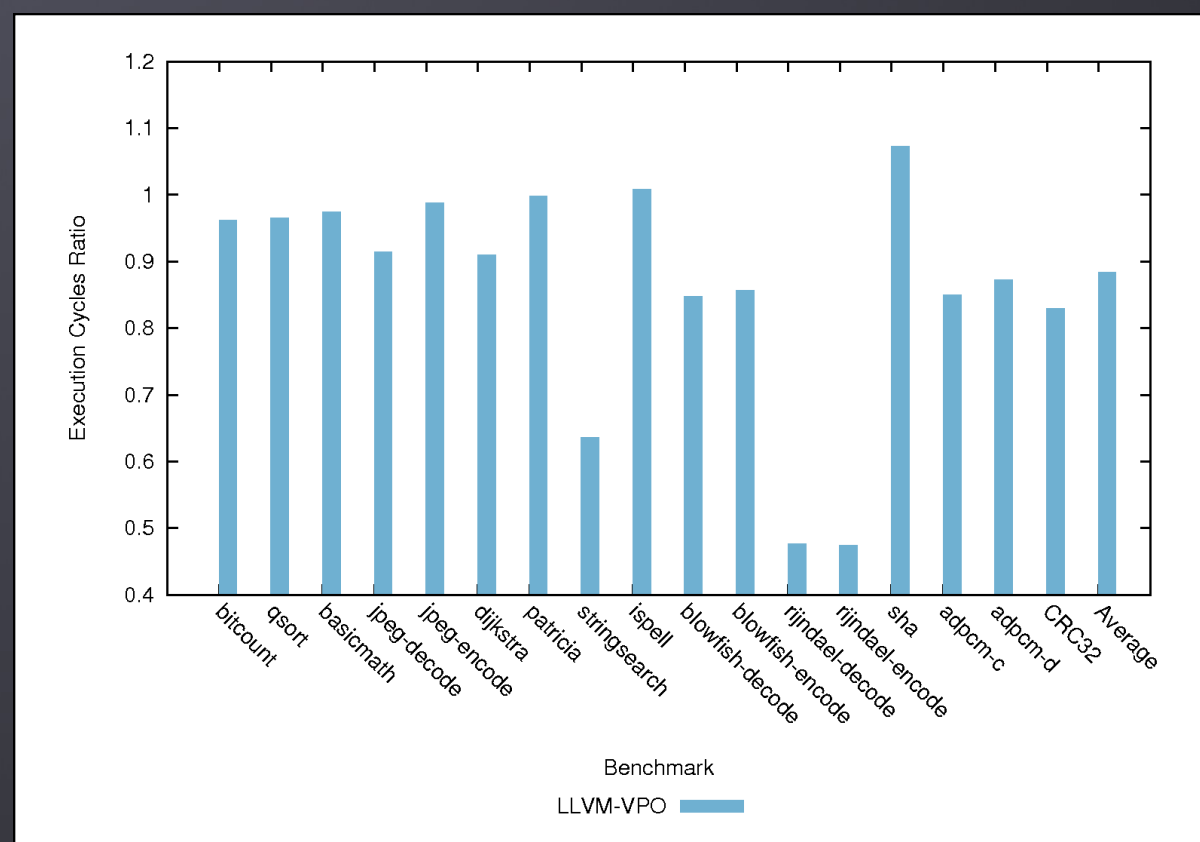
## Project

Previous Attempt	Current Version
<ul style="list-style-type: none"> <li>LLVM Module</li> <li>Easy custom code</li> <li>Performance Loss</li> </ul>	<ul style="list-style-type: none"> <li>LLVM Target</li> <li>Uses LLC with Optimization</li> <li>Performance Loss</li> </ul>
<b>Compilation Process:</b> <pre> graph LR     C --&gt; Clang     Clang -- LLVM IR --&gt; LLVM_OPT[LLVM OPT]     LLVM_OPT -- LLVM IR --&gt; LLVM_VPO_Translator[LLVM-VPO Translator]     LLVM_VPO_Translator -- VPO IR --&gt; VPO     VPO -- ASM --&gt; ASM             </pre>	

## VPO IR Example

File Header:	
Mrfdhsu m registerSize=4 m globalSize = 4 m localSize=4	Architecture Information
-.globl main dmain GLO[1] 0 0	Global Declarations
Function Definitions:	
-main: fmain	Function Definition
Dl0.0_a LOC[0] 2 0 4 0 0	Parameter/Local Definitions
+r[25]=GLO[2] ur[25] A[4] +ST=r[25] ...	Function Calls
+r[2]=0 ur[2] +PC=RT	Return
s=r[2]; * -.end main	Function End

## Current Results



The figure above compares the ratio in execution cycles of our VPO-LLVM compiler to VPO with LCC. On the MiBench Test Suite. Numbers less than 1 indicate performance improvement. The average performance improvement is above 10%.

## Future Goals

- Build a version for ARM
- Build a version for Static Pipeline Architecture [1]
- Fix known performance issues
- Improve Implementation
  - Use Pseudo-Registers
  - Generate Dead Register Lists
  - Simplify target change process
- Handle More Test Cases

## Open Source

The current version of the project is available for SVN checkout. For more information about downloading this project, visit <http://cs.boisestate.edu/~uh/LLVMVPO.htm>

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